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Explicit constructions of graphs without short cycles and low density codes

GA Margulis - Combinatorica, 1982 - Springer

... perform n operations, where n is the order of the parity-check matrix and b ... Since m and q are relatively prime, there exists an integral unimodular matrix ...

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[PDF] ► Pseudorandom construction of low-density parity-check codes using linear congruential sequences

A Prabhakar, K Narayanan - IEEE Transactions on Communications, 2002 - Citeseer

... C_4 . (optional) a is relatively prime to M It should be noted here that since the parity check matrix is not constructed in systematic ...

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On algebraic construction of Gallager and circulant low-density parity-check codes

H Tang, J Xu, Y Kou, S Lin, K Abdel-Ghaffar, PMCS ... - IEEE Transactions on Information Theory, 2004 - ieeexplore.ieee.org

... The parity-check matrix of a code in this class has exactly the same form as that proposed by Gallager [1], [2]. Section IV gives a class of LDPC codes ...

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[CITATION] Some results on quasi-cyclic codes

CL Chen, WW Peterson, EJ Weldon Jr - Information and Control, 1969 - Academic Press.

Cited by 49 - Related articles - Web Search

On lowest-density MDS codes- ► ktfupm.edu.sa [PDF]

M Blaum, RM Roth - IEEE Trans. on Information theory, 1999 - eprints.ktfupm.edu.sa

... By a parity-check matrix (respectively, generator matrix) of an IF ... we mean a parity-check matrix (respectively, generator matrix) over IF q of $(C)IFq$...

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EVENODD: An efficient scheme for tolerating double disk failures in RAID architectures

M Blaum, J Brady, J Bruck, J Menon - IEEE Transactions on computers, 1995 - doi.ieeecomputersociety.org

... A major advantage of EVENODD is that it only requires parity hardware, which is typically present in standard RAID-5 controllers. ...

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Algebraic structure of quasicyclic codes- ► umass.edu [PDF]

K Lally, P Fitzpatrick - Discrete Applied Mathematics, 2001 - Elsevier

... where l is the identity matrix. ... For each i , we check that the diagonal component is a divisor of $x^m - 1$. Then the generator g_i is multiplied by a $ii = (x^m - 1)$...

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Byte-oriented error-correcting codes for semiconductor memory systems

CL Chen - IEEE Transactions on Computers, 1986 - ieeexplore.ieee.org

... A well-known method of constructing SBC-DBD codes is the construction of Reed-Solomon codes with three check bytes [2]-[5]. Let b be the number of bits per byte ...

Cited by 14 - Related articles - Web Search - All 4 versions

Coding of real-number sequences for error correction: A digital signal processing problem

T Marshall Jr - IEEE Journal on Selected Areas in Communications, 1984 - ieeexplore.ieee.org

... An $(N - K) \times L$ - J Fig. 1. Encoder and decoder for a real-number error-correcting code, $X \times N$ parity check matrix H , of rank $N - K$ defined by $GH^* = 0$ (5) ...

Cited by 69 - Related articles - Web Search - All 3 versions


Disk allocation methods using error correcting codes

C Faloutsos, D Metaxas - IEEE Transactions on Computers, 1991 - ieeexplore.ieee.org

... vector U k number of information bits $n - k$ number of parity bits ($= \log_2 m$) ... where p_j is a positive integer relatively prime to m ; Du and Soboleski describe ...

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Key authors: [M Blaum](#) - [G Margulis](#) - [J van Lint](#) - [J Brady](#) - [J Bruck](#)

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